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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Paolo Cappelletti

Patent No.: 6,841,445

Issued: January 11, 2005

For: Method Of Making Floating Gate  
Non-Volatile Memory Cell With Low  
Erasing Voltage Having Double  
Layer Gate Dielectric§  
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Atty. Dkt. No.: NUM.0175D4US

**POWER OF ATTORNEY BY ASSIGNEE**

Under the provisions of 37 C.F.R. § 3.71, the undersigned assignee of record of the entire interest in the above-identified patent/patent application by virtue of an assignment recorded:

Date Recorded 02/17/1998  
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elects to conduct the prosecution of the application/maintenance of the patent to the exclusion of the inventor(s). The undersigned hereby declares that he has reviewed the above-referenced assignment and hereby declares that, to the best of his knowledge, title is in the Assignee, and further declares that all statements made herein of his own knowledge are true and that all statements made on information and belief are believed to be true. The assignee hereby revokes any previous powers of attorney and appoints the practitioners associated with:

**Customer No. 76446**

to prosecute the application identified above, and to transact all business in the United States Patent and Trademark Office connected therewith.

The undersigned is authorized to sign this statement on behalf of the Assignee.

Please direct all communications to: **Customer No. 76446**

Please direct all telephone calls to: Timothy N. Trop at (713) 468-8880.

**ASSIGNEE**  
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